



DATTAKALA GROUP OF INSTITUTIONS
FACULTY OF ENGINEERING
| NAAC Accredited & ISO Certified |

Approved by AICTE - New Delhi, DTE - Mumbai, Recognized by Govt. of Maharashtra
Affiliated to Savitribai Phule Pune University, Pune and M.S.B.T.E., Mumbai

| DTE Code: 6628 | MSBTE Code: 1712 | AICTE ID: 1-5986711 | AISHE Code: C-44576 |

DEPARTMENT OF ELECTRICAL ENGINEERING

Academic Year : 2023-24

TIME TABLE

Sem: II

Class: - B. E.

Class Room No:01207

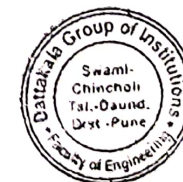
w.e.f.: - 18/12/2023

DAY	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
TIME					
09:00 AM TO 9:55 AM	AEDC	SG	SGP	PROJECT STAGE- II	PROJECT STAGE- II
9:55 AM TO 10:50 AM	SG	AEDC	SG		
10:50 AM TO 11:00 AM	SHORT BREAK				
11:00 AM TO 12:00 PM	SGP	SGP	AEDC	PROJECT STAGE- II	PROJECT STAGE- II
12:00 PM TO 01:00 PM	IE	IE	IE		
01:00 PM TO 01:45 PM	LUNCH BREAK				
01:45 PM TO 02:45 PM	AC- VIII	AEDC(B1)	SGP(B1)	PROJECT STAGE- II	PROJECT STAGE- II
02:45 PM TO 03:45 PM					

Name of Subject	Subject Abbr.	Name of Faculty
Switchgear & Protection	SGP	Prof. Dhumal S.D.
Advanced Electrical Drives & Control	AEDC	Prof. Walke D.B.
Smart Grid	SG	Prof. Tamboli M.S.
Illumination Engineering	IE	Dr. Paratwar P.V.
Project Stage-II	PROJECT STAGE- II	Prof. Bhosale S.S., Prof. Tamboli M.S. Prof. Dhumal S.D.
Audit Course VIII	AC-VIII	Prof. Dhumal S.D.

Time Table Coordinator

H.O.D.



#Principal