



Dattakala Shikshan Sanstha's
DATTAKALA GROUP OF INSTITUTION
FACULTY OF ENGINEERING

[NAAC Accredited & ISO Certified]

541/2, 527, Off Pune-Solapur Highway, Swami-Chincholi (Bhigwan), Tal - Daund, Dist - Pune 413130 Maharashtra
[Email: dkgor@dattakala.edu | Web: www.dattakala.edu.in | Contact No: 9673002923, 9673400500]

DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING

TIME TABLE A.Y. 2024-25 SEM-I

Class: -SE

w.e.f.: -01/07/2024

DAY TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
09:00 AM TO 10:00AM	EC	DSA	DC	DC	M III	
10:00 AM TO 11:00 AM	DSA	EC	M III	DSA	CLASS TEST	
11:00AM TO 11:15AM	SHORT BREAK					
11:15 AM TO 12:15 PM	M III	DC	EC	CO	ESD(B1)	
12:15 PM TO 01:15 PM	CO	M III	CO	CLASS TEST		
01:15 PM TO 02:00 PM	LUNCH BREAK					
02:00 PM TO 03:00 PM	EC(LAB)	DSA(LAB)	CO(LAB)	DC(LAB)	AC	
03:00 PM TO 04:00 PM						

Subject Name	Abbreviation
Engineering Mathematics III	M III
Electronic Circuits	EC
Data Structures & Algorithms	DSA
Digital Circuits	DC
Computer Organization	CO
Electronic Skill Development	ESD
Audit Course	AC

[Signature]
Time Table Coordinator

[Signature]
HOD
E & TC Dept.

[Signature]
Academic Dean

[Signature]
Principal
Dattakala Group of Institutions
Faculty of Engineering
Swami-Chincholi, (Bhigwan)
Daund Dist. Pune-413130

Dattakala Group Of Institutions
Faculty Of Engineering
Swami Chincholi, Tal. Daund
Dist. Pune, Pune - 413130