



Dattakala Shikshan Sanstha's

DATTAKALA GROUP OF INSTITUTIONS

FACULTY OF ENGINEERING

| NAAC Accredited & ISO Certified |

Approved by AICTE - New Delhi, DTE - Mumbai, Recognized by Govt. of Maharashtra

Affiliated to Savitribai Phule Pune University, Pune and M.S.B.T.E., Mumbai

| DTE Code: 6628 | MSBTE Code: 1712 | AICTE ID: I-5986711 | AISHE Code: C-44576 |

Mr. Rama Suryawanshi
Vice - President

Mrs. Maya Zol
Secretary

Prof. Ramdas M. Zol
Founder - President

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

NOTICE

Date: 26/12/2023

Following Staff members are hereby informed that, they are appointed as lab-in charge of respective laboratories of E&TC Engineering Department.

They should take charge.

1. Maintain Dead Stock Register.
2. Observe Lab Cleanliness.
3. Frequently check the instruments.
4. Keep maintenance reports.
5. Display Lab Timetable, Lab Investment, Lab Details.
6. Prepare & Display Charts.
7. Prepare & Display Models.
8. Lab Requirement if any...

SR.NO	NAME OF LAB	NAME OF FACULTY/ STAFF	SIGNATURE
1	Basic Engineering Lab.	Prof. V. S. Tupe	
2	Analog & Digital Lab.	Prof. D. P. Patil	
3	VLSI & Embedded Lab.	Prof. V. S. Pawar	
4	Communication Lab.	Prof. S. N. Vidhate	
5	Simulation Lab	Prof. S. N. Vidhate	



H.O.D.


PRINCIPAL
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